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SHEET 1 OF 1

Form PTO 1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY DOCKET NO. 251148USO		SERIAL NO. 10/809,704	
(Modified)							
APPLICANT							
LIST OF	REFER	RENCES CITED BY APP	PLICANT	Yuko UCHIMARU, et al.			
				FILING DATE		GROUP	
				March 26, 2004			
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
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FOREIGN PATENT DOCUMENTS							
		DOCUMENT	DATE	COUNTRY		TRANSLATION	
21		NUMBER	1010117000			YES	
7	AO	2002-317049	10/31/2002	JAPAN (with English Abstract)			X
V	AP	2968244	08/20/1999	JAPAN (with English Abstract)			X
	AQ	3041424	03/10/2000	JAPAN (with translation of claim 1)			X
A. 4	AR	2002-359240	12/13/2002	JAPAN (with English Abstract)			X
79	AS	2003-119289	04/23/2003	JAPAN (with English Abstract)	-		X
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	AU '						
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OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
m2	AW	International Technology Roadmap for Semiconductors 2002 Update, 2002 Update Tables, page 75, "TABLE 62A MPU INTERCONNECT TECHNOLOGY REQUIREMENTS—NEAR-TERM", 2002					
hu1_	AX	S. SHINMIYAHARA, et al., Cu-interconnect Technology, page 227, "LATEST DEVELOPMENT OF CU WIRING TECHNIQUES", (with partial English translation)					
7	AY						
	AZ	Additional References sheet(s) atta					erences sheet(s) attached
Examiner	mue 2.	الماس		Date Considered 6/4/67			
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							